

F#6.1

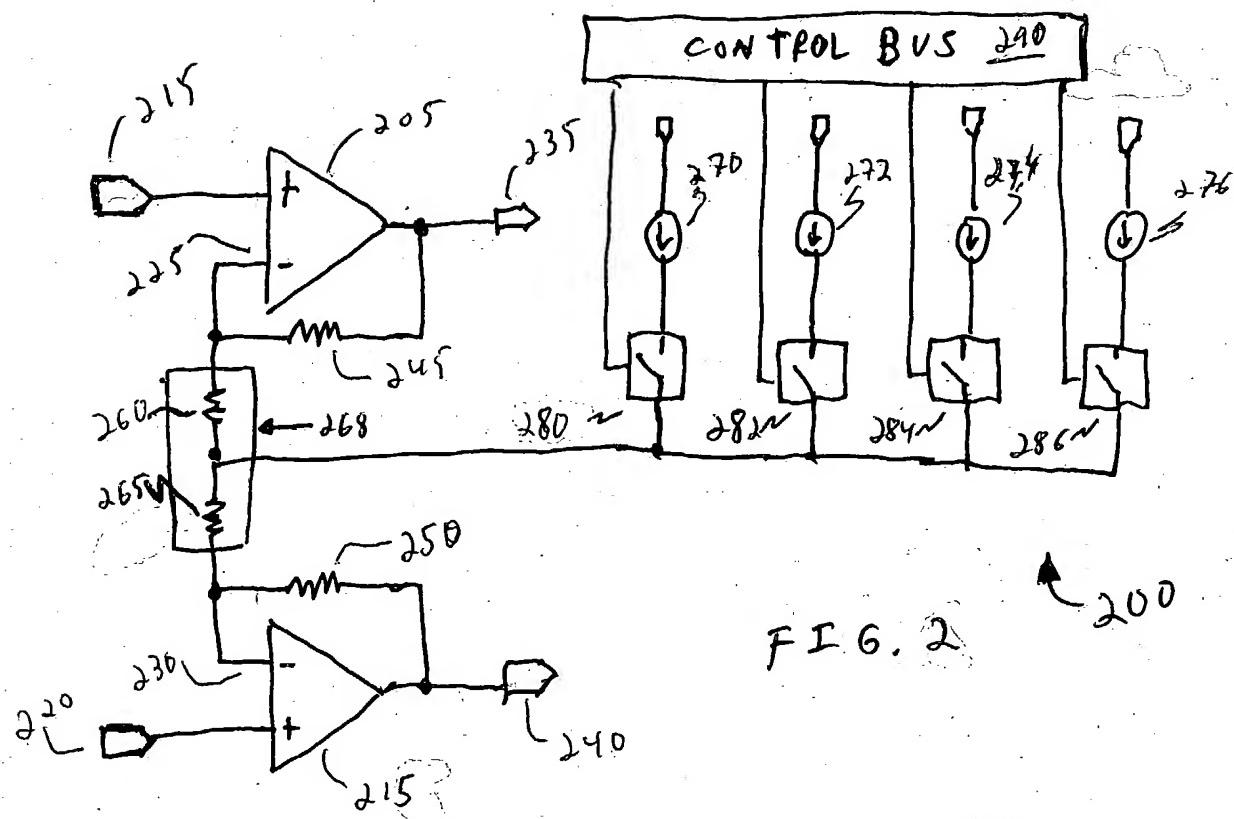


FIG. 2

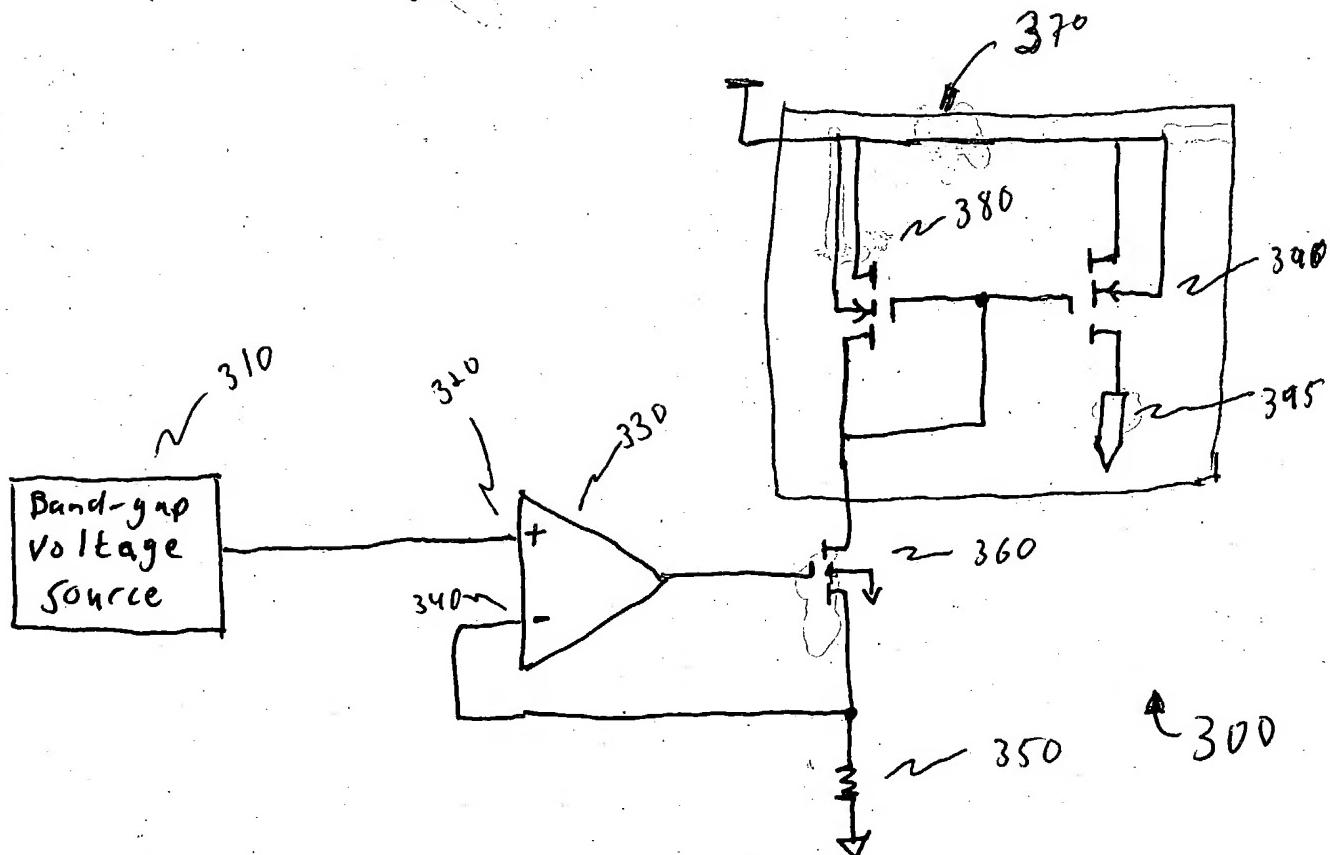


FIG. 3

FIG. 4A

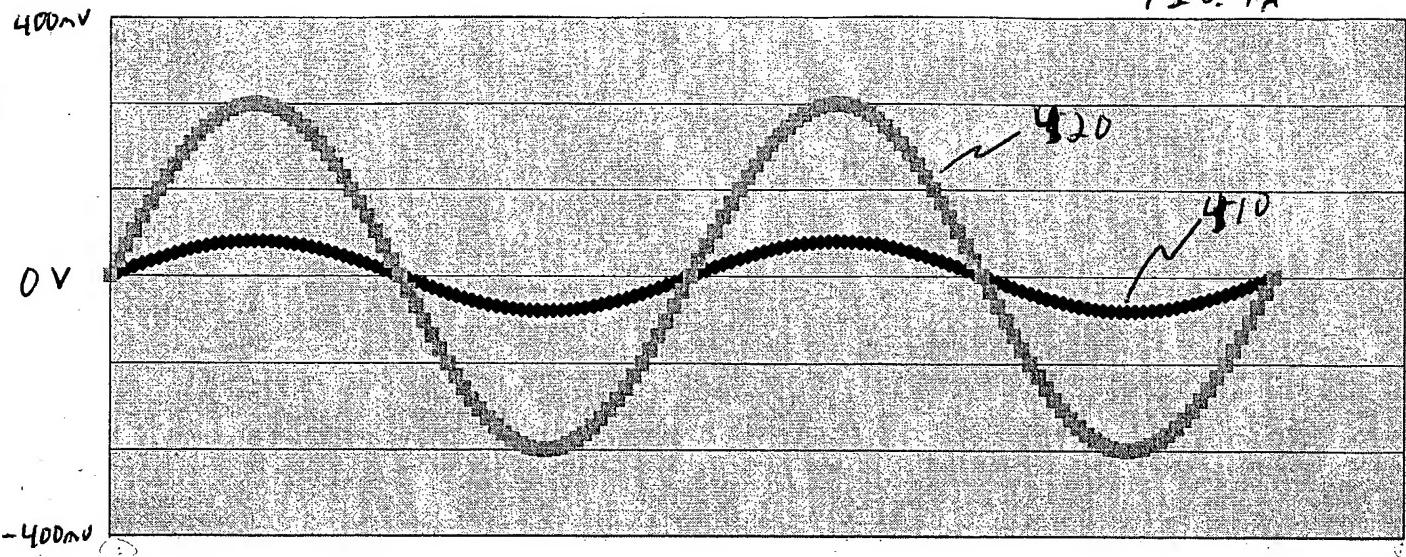


FIG. 4B

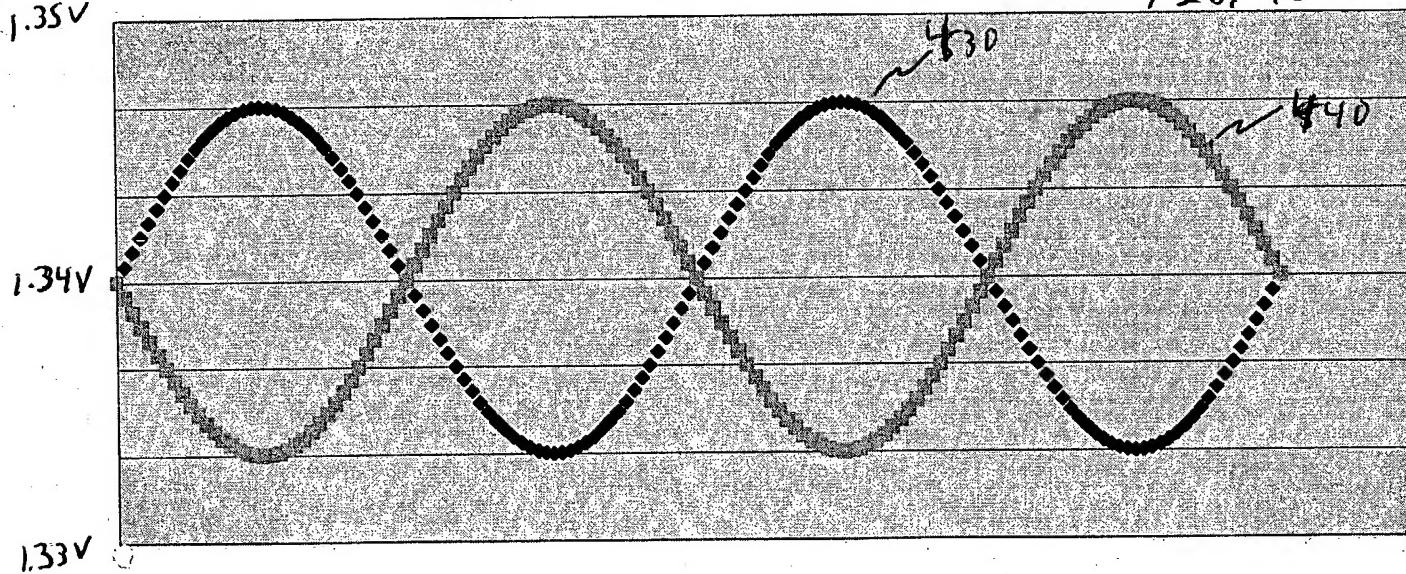
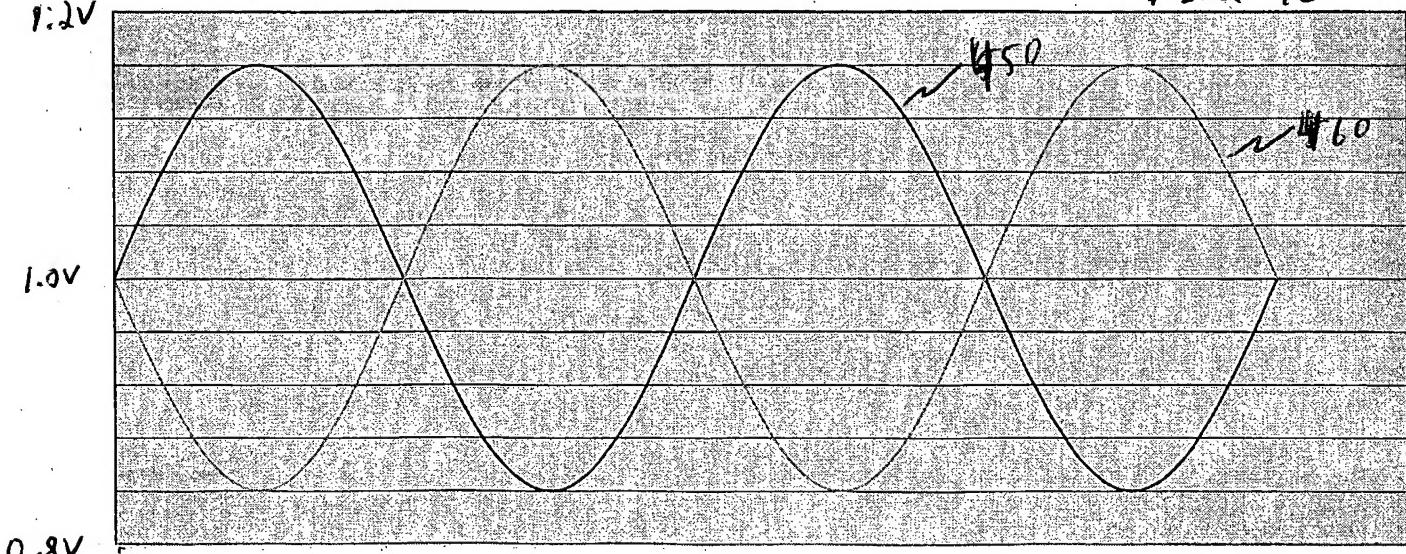


FIG. 4C



50°

FIG. 5

APPLY FIRST DIFFERENTIAL SIGNAL HAVING A FIRST COMMON-MODE VOLTAGE TO FIRST NON-INVERTING INPUT OF DIFFERENTIAL AMPLIFIER

V

APPLY SECOND DIFFERENTIAL SIGNAL HAVING THE FIRST COMMON-MODE VOLTAGE TO SECOND NON-INVERTING INPUT OF DIFFERENTIAL AMPLIFIER

V

GENERATE A SUBSTANTIALLY FIXED CURRENT

V

COMMUNICATE SUBSTANTIALLY FIXED CURRENT TO FIRST AND SECOND INVERTING INPUTS OF DIFFERENTIAL AMPLIFIER IN PREDETERMINED PORTIONS

V

APPLY FIRST AND SECOND FEEDBACK SIGNALS TO FIRST AND SECOND INVERTING INPUTS

V

PRODUCE FIRST AND SECOND DIFFERENTIAL OUTPUT SIGNALS HAVING A SECOND COMMON-MODE VOLTAGE

~ 510

~ 520

~ 530

~ 540

~ 550

~ 560